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Cursuri predate

[Introducere în calculatoare](#)

[Bazele sistemelor de achiziție a datelor](#)

[Tehnici avansate de sinteză a sistemelor digitale complexe](#)

Domenii de interes

Prelucrarea numerică a semnalelor

Arhitecturi VLSI pentru DSP

Sisteme de achiziție a datelor

Publicații selectate

- [*] D.F. Chiper, ["A Structured Dual Split-Radix Algorithm for the Discrete Hartley Transform"](#), *IEEE Transactions on Circuits, Systems and Signal Processing*, vol. 54, no. 10, pp. 2347-2358, 2002.
- [1] D.F. Chiper, M.N.S. Swamy, M.O. Anis, "A Unified Framework for the VLSI Implementation of Memory-Based Systolic Algorithms," *IEEE Transactions on Circuits, Systems, and Signal Processing*, vol. 55, no. 10, pp. 2347-2358, 2007.
- [2] D.F. Chiper, "A New Memory-Based Systolic Algorithm with Low Complexity," *IEEE Transactions on Circuits, Systems, and Signal Processing*, vol. 53, no. 10, pp. 2347-2358, 2005.
- [3] D.F. Chiper, M.N.S. Swamy, M.O. Anis, "A New Systolic Algorithm and a Memory-Based Systolic Array for the VLSI Implementation of the Discrete Sine Transform," *IEEE Transactions on Circuits, Systems, and Signal Processing*, vol. 53, no. 10, pp. 2347-2358, 2005.
- [4] D.F. Chiper, M.N.S. Swamy, M.O. Anis, "A New Systolic Array Algorithm for the VLSI Implementation of the Discrete Sine Transform," *IEEE Transactions on Circuits, Systems, and Signal Processing*, vol. 53, no. 10, pp. 2347-2358, 2005.
- [5] D.F. Chiper, M.N.S. Swamy, M.O. Anis, "A New Systolic Array Algorithm for the VLSI Implementation of the Discrete Sine Transform," *IEEE Transactions on Circuits, Systems, and Signal Processing*, vol. 53, no. 10, pp. 2347-2358, 2005.

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